



PORTO, PORTUGAL
SEPT. 2-4, 2013



CALL FOR SUBMISSIONS

PAPERS, PhD FORUM, DEMOS, WORKSHOPS / TUTORIALS, and BOF SESSIONS

General Chair

João M. P. Cardoso, University of Porto, Portugal

Program Co-Chairs

Katherine (Compton) Morrow, University of Wisconsin-Madison, USA

Pedro Diniz, USC Information Sciences Institute, USA

Steering Committee

Apostolos Dollas, Technical University of Crete, Greece
Eduardo Boemo, Universidad Autonoma de Madrid, Spain
Fabrizio Ferrandi, Politecnico di Milano, Italy
Jari Nurmi, Tampere University of Technology, Finland
Jim Tøresen, University of Oslo, Norway
John Gray, Independent Consultant, UK
Jürgen Becker, University of Karlsruhe, Germany
Koen Bertels, Technical University of Delft, Netherlands
Lionel Torres, University of Montpellier II, France
Manfred Glesner, Darmstadt University of Technology, Germany
Martin Danek, UTIA AV CR, Czech Republic
Patrick Lysaght, Xilinx, Inc., USA
Peter Y.K. Cheung, Imperial College London, UK
Reiner Hartenstein, University of Kaiserslautern, Germany
Roger Woods, Queen's University of Belfast, UK
Udo Kebschull, University of Heidelberg, Germany
Wayne Luk, Imperial College, London, UK

The International Conference on Field Programmable Logic and Applications (FPL) is the first and largest conference covering the rapidly growing and evolving area of field-programmable logic. During the past 22 years, many of the advances achieved in reconfigurable architectures, applications, design methods and tools have been first published in the proceedings of the FPL conference series. Its objective is to bring together researchers and practitioners from both academia and industry from all over the world for broad discussions and exchanges on field-programmable logic, including, but not limited to: applications, advanced electronic design automation (EDA), novel system architectures, embedded processors, arithmetic, and dynamic reconfiguration.

FPL is organized yearly in Europe and attended by international leading scientists and researchers. The 23rd FPL continues the tradition of the previous editions. It will take place September 2-4, 2013 in Porto, Portugal - a UNESCO World Heritage Site. FPL2013 will be hosted by the University of Porto, Faculty of Engineering (FEUP), including the Informatics Engineering Department and the Electrical and Computer Engineering Department. The conference will take place at the HF Ipanema Park Hotel****, located in the center of Porto.

The Program Committee cordially invites you to participate and submit your contribution to FPL 2013. The conference topics include, but are not limited to:

Architectures

Adaptive communication & NoCs
Defect- and fault tolerance
Field programmable analog arrays
FPGAs vs. Multicores / GPUs
Low power architectures
Partial, run-time reconfiguration
Reconfigurable embedded systems
Self-awareness, self-adaptivity
Self-testing, self-healing

Applications

Aerospace and automotive
Application acceleration
Bioinformatics & bio-inspired systems
Communications and networking
Finance and database acceleration
HPC
Medical
Physics and astronomy
Security
Signal processing and SDR

Design Methods and Tools

Compilers and languages
Hardware / software co-design
Logic optimization and technology mapping
Open source tools
Partial, run-time reconfiguration
Placement and routing algorithms
Radiation tolerance and reliability
Rapid prototyping and emulation
System-level design tools
Testing, verification and benchmarks

Surveys, Trends and Education

Emerging device technologies
History/surveys of reconfigurable logic
Million LUTs- / thousand cores designs
Open source designs
Roadmap of reconfigurable computing
Teaching courses and tutorials

For information about the specific calls, please visit the FPL2013 website: <http://www.fpl2013.org>
All contributions must be submitted electronically in PDF format via the FPL2013 submissions website: <https://www.softconf.com/d/fpl2013/>

Papers should be prepared following the guidelines and templates available at: <http://www.fpl2013.org>

PAPERS

For additional information about the submission of regular papers, please contact the FPL2013 PC Chairs:
- Katherine (Compton) Morrow, University of Wisconsin-Madison, USA (kati@engr.wisc.edu)
- Pedro C. Diniz, USC Information Sciences Institute, USA (pedro@isi.edu)

CONTRIBUTIONS TO PhD FORUM

For additional information about the PhD Forum, please contact the FPL2013 PhD Forum Chairs:
- Miriam Leeser, Northeastern University, USA (mel@coe.neu.edu)
- Stephan Wong, TU Delft, The Netherlands (j.s.s.m.wong@tudelft.nl)

WORKSHOPS AND TUTORIALS

For additional information about the submission of Workshop and Tutorial proposals, please contact the FPL2013 Workshop/Tutorial Chairs:
- Cristina Silvano, Politecnico di Milano, Italy (silvano@elet.polimi.it)
- Michael Hübner, University of Bochum, Germany (Michael.Huebner@ruhr-uni-bochum.de)

DEMOS (DEMO NIGHT)

For additional information about the submission of demo proposal, please contact the FPL2013 Demo Night Chair:
- José Gabriel de F. Coutinho, Imperial College London, UK (Gabriel.Figueiredo@imperial.ac.uk)

BIRDS OF A FEATHER (BOF) PROPOSALS

For additional information about the submission of BOF proposals, please contact the FPL2013 BOF Session Chair:
- Christian Plessl, University of Paderborn, Germany (christian.plessl@uni-paderborn.de)
- Kyle Rupnow, Nanyang Technological University, Singapore (k.rupnow@ntu.edu.sg)

web www.fpl2013.org

Important Dates:

Regular Papers and PhD Forum submissions due: March 29, 2013
(strict extended deadline, 23:59, UTC time zone)

Notification of acceptance: May 28, 2013
Camera ready copy: June 24, 2013
Early registration deadline: June 24, 2013

Workshop/Tutorials proposals due: April 12, 2013
Notification of acceptance of Workshops/Tutorials proposals: April 26, 2013

BOF session proposals due: April 12, 2013
Notification of acceptance of BOF proposals: April 26, 2013

Demonstrators (Demo Night) due: July 5, 2013
Notification of acceptance for demonstrators: July 19, 2013

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CALL FOR PAPERS

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Papers should be prepared using the guidelines and templates available at: <http://www.fpl2013.org>

REGULAR PAPERS

Authors are invited to submit original and unpublished contributions as 6 page papers (in English) in IEEE double-column format to be considered as regular papers (two extra pages are allowed, if needed). Submissions accepted as posters will have 4 pages.

We highly encourage authors to make their research results – including systems, benchmarks, or tools – freely and publicly available in order to gain research and design productivity within the FPL community. We further invite authors to present working demo systems or tools.

Authors of selected papers will be invited to submit extended versions for consideration for publication in a special issue of ACM Transactions on Reconfigurable Technology and Systems (TRETs) dedicated to FPL2013.

Important Dates:

Paper submission due: March 29, 2013
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Camera ready copy: June 24, 2013
Early registration deadline: June 24, 2013

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CONTRIBUTIONS TO PhD FORUM

FPL's PhD forum in 2013 welcomes two types of submissions. First, the forum is intended to be a venue for PhD students to present their innovative (but still preliminary) work and receive early feedback from other researchers. Second, it is intended to provide PhD students in the final stages of their work with an opportunity to give a broad overview of their work and draw more attention to it from the academic and industrial worlds.

PhD forum authors are invited to submit 2-page papers in IEEE double column format. We strongly suggest that those papers include clear descriptions of key thesis criteria, such as motivation, objectives, problem definition, addressed solutions, current status, and planned work. Contributions to the forum based on preliminary results or work in progress are particularly encouraged.

Papers accepted to the PhD forum (2 pages) will be published in the conference proceedings and presented at a special poster session during the conference. During a special presentation slot, presenters can draw attention for their poster through a short "elevator pitch" (2 minute presentation).

For additional information about the PhD Forum, please contact the FPL2013 PhD Forum Chairs:

- Miriam Leeser, Northeastern University, USA (mel@coe.neu.edu)
- Stephan Wong, TU Delft, The Netherlands (j.s.s.m.wong@tudelft.nl)

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CALL FOR WORKSHOPS / TUTORIALS

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Million LUTs- / thousand cores designs
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Roadmap of reconfigurable computing
Teaching courses and tutorials

WORKSHOPS AND TUTORIALS

FPL2013 will include academic and industrial tutorials and workshops as satellite events, targeting hot topics, projects and tools. These events will be organized by academic participants or industrial sponsors and will take place at the Faculty of Engineering of the University of Porto, planned for September 1st (the day before FPL2013), 5th, 6th and 7th (on the 3 days after FPL2013).

The duration of an FPL Workshop and Tutorial is typically half-day, 9:00 AM to 12:00 AM, or 2:00 PM to 5:30 PM, including one half-hour coffee break, or full-day from 9:00 AM to 5:30 PM, including an one-hour lunch break and two half-hour coffee breaks.

**Submission Instructions for Workshop and Tutorials at FPL2013:

Submit a 1-page or to 2-page Workshop proposal including:

- Workshop title and description of its main objectives and its relevance to the FPL community;
- Short biography for each organizer, including the relevant recent activity to the topic of the Workshop;
- Tentative format for the workshop, including possible special sessions, invited talks, panels, regular paper sessions, and poster sessions;
- Names and affiliations of the invited speakers;
- If a call for papers and/or posters is planned, a tentative submission deadline and notification deadline should be included;
- Any special requirements the Workshop may have;
- Preferred date for the Workshop.

Submit a 1-page or 2-page Tutorial proposal including:

- Tutorial title, duration, a description of its main objectives and topics, and its relevance to the FPL community;
- Tentative format for the tutorial;
- Short biography for each presenter, including the relevant recent activity to the topic of the Tutorial;
- Any special requirements the Tutorial may have.
- Preferred date for the Tutorial.

For additional information about the submission of Workshop and Tutorial proposals, please contact the FPL2013 Workshop/Tutorial Chairs:

- Cristina Silvano, Politecnico di Milano, Italy (silvano@elet.polimi.it)
- Michael Hübner, University of Bochum, Germany (Michael.Huebner@ruhr-uni-bochum.de)

web

www.fpl2013.org

Important Dates:

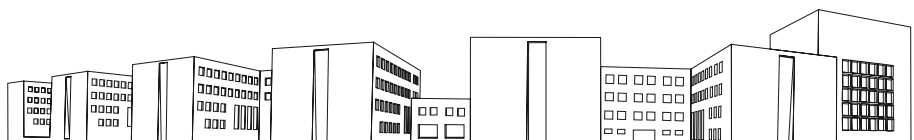
Early registration deadline:	June 24, 2013
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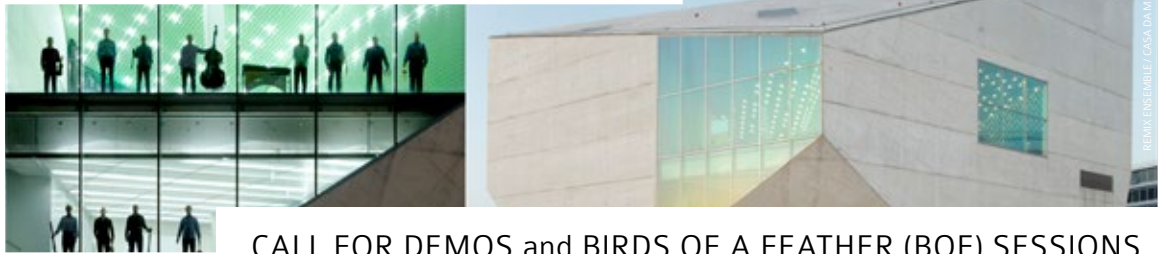
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CALL FOR DEMOS and BIRDS OF A FEATHER (BOF) SESSIONS

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Important Dates:

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BOF session proposals due: April 12, 2013
Notification of acceptance for BOF proposals: April 26, 2013

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DEMOS (DEMO NIGHT)

Authors and other participants are also invited to demonstrate and disseminate their work at the Demo night. The Demo night will take place on Monday, September 2nd, and will include hors d'oeuvres and drinks. This demo session provides an excellent opportunity to increase visibility, to interact with attendees and get their feedback. Demonstrations may include commercial and academic research-oriented tools, platforms and systems. To propose a demonstration at the Demo night, an abstract must be submitted with the information regarding to what problems are being addressed and relevance to FPL.

Submit a 2-page demo proposal in IEEE double column format. The first page should include the following elements:

- Demo title, a description of its main objectives and topics, and its relevance to the FPL community;
- Name and affiliation and short biography for each presenter;
- Any special requirements the demonstration may have.

A second page should include an abstract of the demo itself along with the title, authors (not just the presenters) as well as their affiliations. The single-page abstracts will be included in the FPL2013 proceedings.

Demo proposals should be prepared using the guidelines and templates available at: <http://www.fpl2013.org>
For additional information about the submission of demo proposal, please contact the FPL2013 Demo Night Chair:

- José Gabriel de F. Coutinho, Imperial College London, UK (Gabriel.Figueiredo@imperial.ac.uk)

BIRDS OF A FEATHER (BOF) PROPOSALS

In FPL2013, we plan to include Birds of a Feather (BOF) sessions, as a way to provide more opportunities for exchanging and discussing ideas. Those BOF sessions will typically consist of 1-2 hour sessions which bring together people with a shared interest in a particular research area. Examples of BOF sessions can be: high-level synthesis, dynamic reconfiguration, or non-silicon technology. BOF sessions are organized as an open lively informal discussion meeting moderated by a chair. The way to kick off the session can vary widely, e.g., some controversial 5 minute talks by the organizers, short demos of interesting technologies. The informal nature of BOF sessions will be also emphasized by providing drinks and snacks during these sessions.

The BOF sessions are also an easy way to help young researchers and participants not actively engaged in the FPL community to get in touch with participants sharing similar research and application interests.

The submission of a BOF session proposal (as a 1-page or 2-page proposal) should include the following:

- BOF session title, a description of its main objectives and topics, and its relevance to the FPL community;
- Short biography for each organizer, including the relevant recent activity to the topic of the BOF session;
- Any special requirements the BOF session may have.

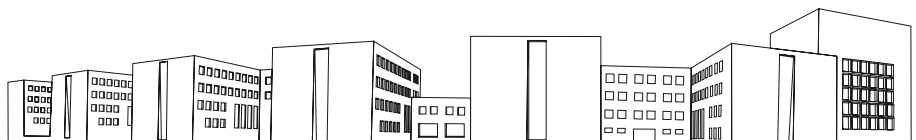
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COMMITTEES

ORGANIZING COMMITTEE

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Katherine (Compton) Morrow, University of Wisconsin-Madison, Madison, USA (kati@engr.wisc.edu)

Pedro Diniz, USC Information Sciences Institute, USA (pedro@isi.edu)

Finance Chair:

José Carlos Alves, University of Porto, FEUP, Portugal

Proceedings Chair:

João Canas Ferreira, University of Porto, Portugal

Publicity Co-Chairs:

Dimitrios Soudris, National Technical University of Athens, Greece (Europe and Africa)

Eduardo Marques, University of São Paulo/ICMC, Brazil (Central and South America)

Lesley Shannon, Simon Fraser University, Canada (North America)

Ray Cheung, City University of Hong Kong, Hong Kong (Asia and Australia)

Workshops Co-Chairs:

Cristina Silvano, Politecnico di Milano, Italy

Michael Hübner, University of Bochum, Germany

PhD Forum Co-Chairs:

Miriam Leeser, Northeastern University, USA

Stephan Wong, TU Delft, The Netherlands

Sponsor and Exhibition Chairs:

Fernando Gonçalves, Coreworks S.A., Lisboa, Portugal

Manuel Gericota, ISEP, Porto, Portugal

Demo Night Chair:

José G. Coutinho, Imperial College London, UK

Birds of a Feather (BOF) Chairs:

Christian Plessl, University of Paderborn, Germany

Kyle Rupnow, Nanyang Technological University, Singapore

Track Architectures:

Horácio Neto, INESC-ID/IST, Portugal

Ray Cheung, City University of Hong Kong, Hong Kong

Track Applications:

Teresa Riesgo, Universidade Politécnica de Madrid (UPM), Spain

Ryan Kastner, University of California, USA

Track Design Methods and Tools:

Guy Gogniat, Université de Bretagne-Sud - UEB, France

Luigi Carro, UFRGS, Brazil

Track Surveys, Trends and Education:

André DeHon, University of Pennsylvania, USA

Marco D. Santambrogio, Politecnico di Milano, Italy

Webmaster:

João M. P. Cardoso, University of Porto, Portugal

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PROGRAM COMMITTEE

Norbert Abel, University of Heidelberg, Germany

Hideharu Amano, Keio University, Japan

Jason Anderson, University of Toronto, Canada

David Andrews, University of Kansas, USA

Kubilay Atas, IBM Zurich, Switzerland

Peter Athanas, Virginia Tech, USA

Juergen Becker, University of Karlsruhe, Germany

Pascal Benoit, LIRMM CNRS / Univ. of Montpellier 2, France

Mladen Berekovic, University of Braunschweig, Germany

Neil Bergmann, University of Queensland, Australia

Koen Bertels, Technical University of Delft, The Netherlands

Dustyn Blasig, National Instruments, USA

Michaela Blott, Xilinx, USA

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Christos-Savvas Bouganis, Imperial College London, UK

Eli Bozorgzadeh, University of California, Irvine, USA

Gordon Brebner, Xilinx, USA

Philip Brisk, UC Riverside, USA

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